

EXPRESS MAIL LABEL NO.: EV 4858070900 US

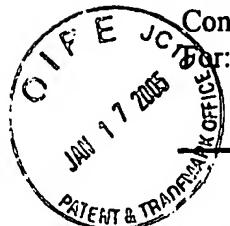
Form PTO-1449

Sheet 1 of 4

Applicant: Hofmeister, et al.  
Serial No.: 10/697,395  
Filing Date: October 30, 2003  
Confirmation No.: 7465

Att'y Docket No.: 15436.247.11.1  
Group: 2661

AUTOMATIC SELECTION OF DATA RATE FOR OPTOELECTRIC  
DEVICES



SUPPLEMENTAL INFORMATION DISCLOSURE CITATIONS MADE BY APPLICANT

U.S. Patent Documents

<u>Examiner Initial*</u>	<u>Document Number</u>	<u>Issue/Publication Date</u>	<u>Name</u>
KJM 1	5,854,704	12/29/1998	Grandpierre
KJM 2	6,256,127 B1	07/03/2001	Taylor
KJM 3	4,932,038	06/05/1990	Windus
KJM 4	2002/0021468 A1	02/21/2002	Kato et al.
KJM 5	2002/0060824 A1	05/23/2002	Liou et al.
KJM 6	2003/0053170 A1	03/20/2003	Levinson et al.

Foreign Patent Documents

NONE

Examiner:

Date Considered: 1/17/06

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

EXPRESS MAIL LABEL NO.: EV 4858070900 US

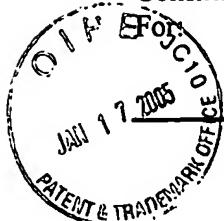
Form PTO-1449

Sheet 2 of 4

Applicant: Hofmeister, et al.  
Serial No.: 10/697,395  
Filing Date: October 30, 2003  
Confirmation No.: 7465

Att'y Docket No.: 15436.247.11.1  
Group: 2661

AUTOMATIC SELECTION OF DATA RATE FOR OPTOELECTRIC  
DEVICES



Other Documents  
(including author, title, pertinent pages, etc.)

Examiner  
Initial\*

- KJM 7 *LXT16706/16707 SerDes Chipset*, Intel Products,  
[www.intel.com/design/network/products/optical/phys/lxt16706.htm](http://www.intel.com/design/network/products/optical/phys/lxt16706.htm), April 19,  
2002.
- KJM 8 *LXT35401 XAUI-to-Quad 3.2G Transceiver*, Intel Products,  
[www.intel.com/design/network/products/optical/phys/lxt35401.htm](http://www.intel.com/design/network/products/optical/phys/lxt35401.htm), April 19,  
2002.
- KJM 9 Texas Instruments User's Guide, *TLK2201 Serdes EVM Kit Setup and Usage*,  
Mixed Signal DSP Solutions, July 2000.
- KJM 10 Texas Instruments User's Guide, *TLK1501 Serdes EVM Kit Setup and Usage*,  
Mixed Signal Products, June 2000.
- KJM 11 National Semiconductor DS92LV16 Design Guide, *Serializing Made Simple*,  
February 2002.
- KJM 12 Vaishali Semiconductor, *Fibre Channel Transceiver*, VN16117, MDSN-0002-02,  
08/09/2001.
- KJM 13 Fairchild Semiconductor, Application Note 77, *CMOS, the Ideal Logic Family*,  
January 1983.

Examiner: *Ron Zabel*

Date Considered: *1/17/06*

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

EXPRESS MAIL LABEL NO.: EV 4858070900 US

Form PTO-1449

Sheet 3 of 4

Applicant: Hofmeister, et al.

Serial No.: 10/697,395

Att'y Docket No.: 15436.247.11.1

Filing Date: October 30, 2003

Group: 2661

Confirmation No.: 7465

For: AUTOMATIC SELECTION OF DATA RATE FOR OPTOELECTRIC  
DEVICES

---

HJM 14      Analog Target Specification, Annex 48B, Published by IEEE New York, 05/2001,  
pp. 6-14.

---

Examiner: 

Date Considered: 1/17/06

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

**EXPRESS MAIL LABEL NO.: EV 4858070900 US**

**Form PTO-1449**

**Sheet 4 of 4**

Applicant: **Hofmeister, et al.**  
Serial No.: **10/697,395** Att'y Docket No.: **15436.247.11.1**  
Filing Date: **October 30, 2003** Group: **2661**  
Confirmation No.: **7465**  
For: **AUTOMATIC SELECTION OF DATA RATE FOR OPTOELECTRIC  
DEVICES**

---

**References Cited by Applicants**

While the filing of Information Disclosure Statements is voluntary, the procedure is governed by the guidelines of Section 609 of the Manual of Patent Examining Procedure and 37 C.F.R. §§ 1.97 and 1.98. To be considered a proper Information Disclosure Statement, Form PTO-1449 shall be accompanied by a copy of each listed patent or publication or other item of information and a translation of the pertinent portions of foreign documents (if an existing translation is readily available to the applicant), an explanation of relevance of each reference not in the English language, and should be submitted in a timely manner as set out in MPEP Sec. 609.

Examiners will consider all citations submitted in conformance with 37 C.F.R. § 1.98 and MPEP Sec. 609 and place their initials adjacent the citations in the spaces provided on this form. Examiners will also initial citations not in conformance with the guidelines which may have been considered. A reference may be considered by the Examiner for any reason whether or not the citation is in full conformance with the guidelines. A line will be drawn through a citation if it is not in conformance with the guidelines AND has not been considered. A copy of the submitted form, as reviewed by the Examiner, will be returned to the applicant with the next communication. The original of the form will be entered into the application file.

Each citation initialed by the Examiner will be printed on the issued patent in the same manner as references cited by the Examiner on Form PTO-892.

The reference designations "A1," "A2," etc. (referring to Applicant's reference 1, Applicant's reference 2, etc.) will be used by the Examiner in the same manner as Examiner's reference designations "A," "B," "C," etc. on Office Action Form PTO-1142.

TAT000000592V001

---

**Examiner:**

**Date Considered:**

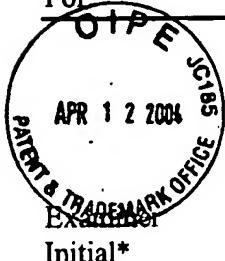
\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant: Hofmeister, et al  
 Serial No.: 10/697,395  
 Filing Date: October 30, 2003  
 For:

AUTOMATIC SELECTION OF DATA RATE FOR OPTOELECTRONIC DEVICES

Att'y Docket No.: 15436.247.11.1

Art Unit: 2661

INFORMATION DISCLOSURE CITATIONS MADE BY APPLICANTU.S. Patent Documents

<u>Examiner Initial*</u>	<u>Document Number</u>	<u>Issue/Publication Date</u>	<u>Name</u>
--------------------------	------------------------	-------------------------------	-------------

Foreign Patent Documents

<u>Examiner Initial*</u>	<u>Document Number</u>	<u>Publication Date</u>	<u>Country or Patent Office</u>	<u>Translation</u>
--------------------------	------------------------	-------------------------	---------------------------------	--------------------

Prior Applications Filed

(including serial number, title, filing date, etc.)

<u>Examiner Initial*</u>	<u>Serial No.</u>	<u>Date</u>	<u>Title</u>
<u>KMM</u> 1	10/629,301	07/28/2003	Transceiver Module and Integrated Circuit With Multi-Rate Eye Openers and Bypass

References Cited by Applicants

While the filing of Information Disclosure Statements is voluntary, the procedure is governed by the guidelines of Section 609 of the Manual of Patent Examining Procedure and 37 C.F.R. §§ 1.97 and 1.98. To be considered a proper Information Disclosure Statement, Form PTO-1449 shall be accompanied by a copy of each listed patent or publication or other item of information and a translation of the pertinent portions of foreign documents (if an existing translation is readily available to the applicant), an explanation of relevance of each reference not in the English language, and should be submitted in a timely manner as set out in MPEP Sec. 609.

Examiners will consider all citations submitted in conformance with 37 C.F.R. § 1.98 and MPEP Sec. 609 and place their initials adjacent the citations in the spaces provided on this form. Examiners will also initial citations not in conformance with the guidelines which may have been considered. A reference may be considered by the Examiner for any reason whether or not the citation is in full conformance with the guidelines. A line will be drawn through a citation if it is not in conformance with the guidelines AND has not been considered. A copy of the submitted form, as reviewed

Examiner:

A handwritten signature in black ink, appearing to read "Jan Mueller".

Date Considered:

1/17/06

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant: Hofmeister, et al  
Serial No.: 10/697,395  
Filing Date: October 30, 2003  
For: AUTOMATIC SELECTION OF DATA RATE FOR OPTOELECTRONIC DEVICES

Att'y Docket No.: 15436.247.11.1  
Art Unit: 2661

by the Examiner, will be returned to the applicant with the next communication. The original of the form will be entered into the application file.

Each citation initialed by the Examiner will be printed on the issued patent in the same manner as references cited by the Examiner on Form PTO-892.

The reference designations "A1," "A2," etc. (referring to Applicant's reference 1, Applicant's reference 2, etc.) will be used by the Examiner in the same manner as Examiner's reference designations "A," "B," "C," etc. on Office Action Form PTO-1142.

W:\15436\247.11.1\ULW0000000858V001.doc

---

Examiner:

Date Considered:

---

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

---